Ji Eun JANG
"Test Mode Circuit of Semiconductor
Memory Device"
Attorney Docket No.: 40296-0032

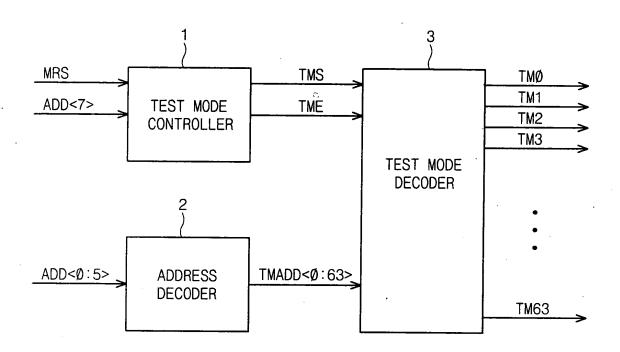


Fig.1 (Prior Art)

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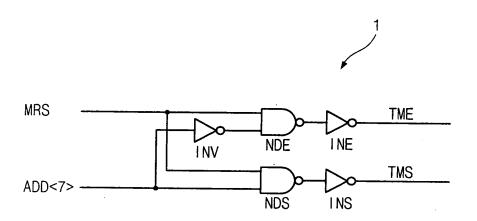


Fig.2 (Prior Art)

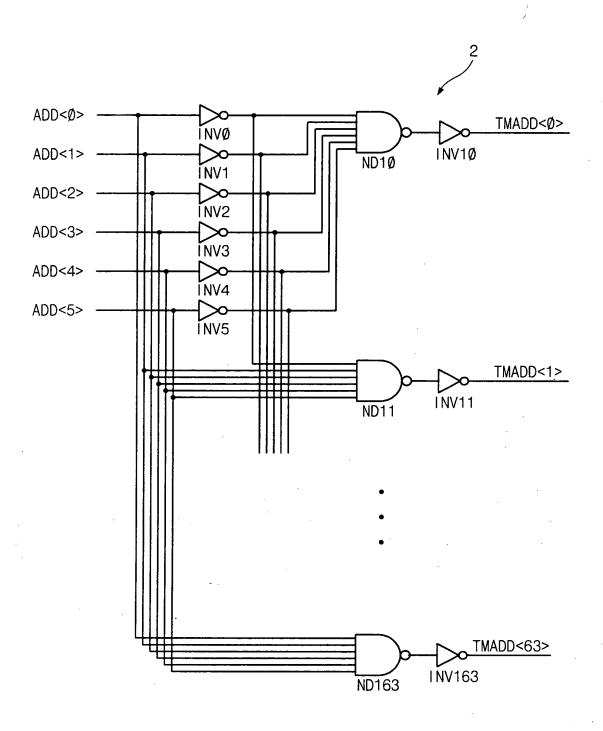


Fig.3 (Prior Art)

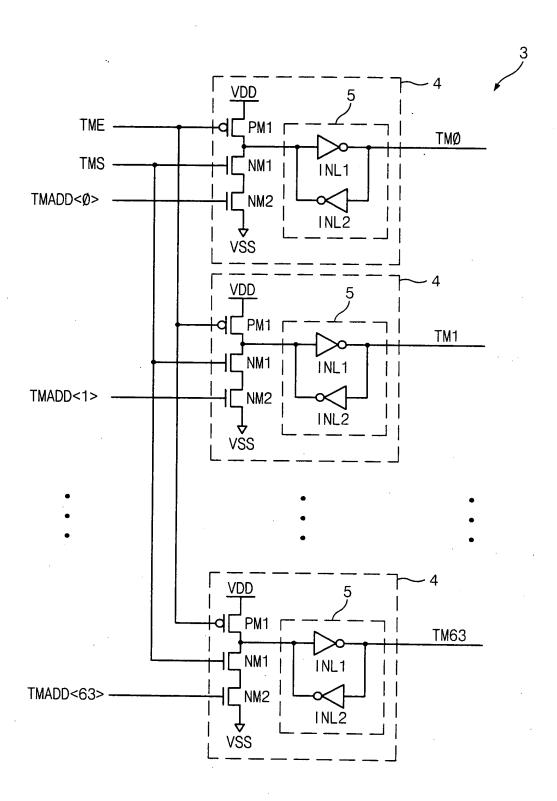


Fig.4 (Prior Art)

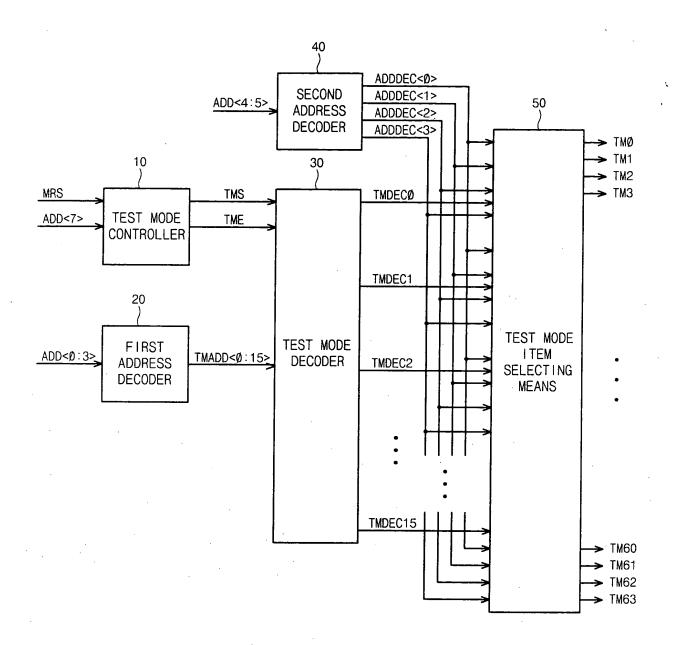


Fig.5

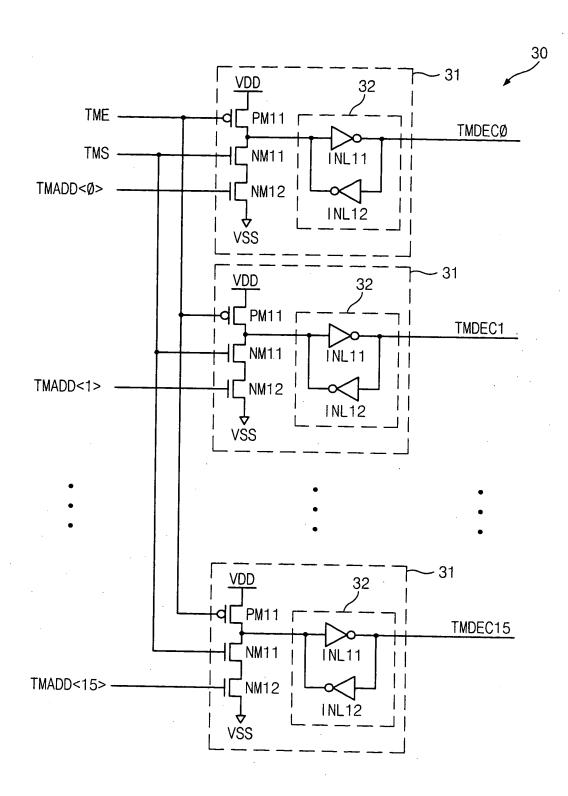


Fig.6

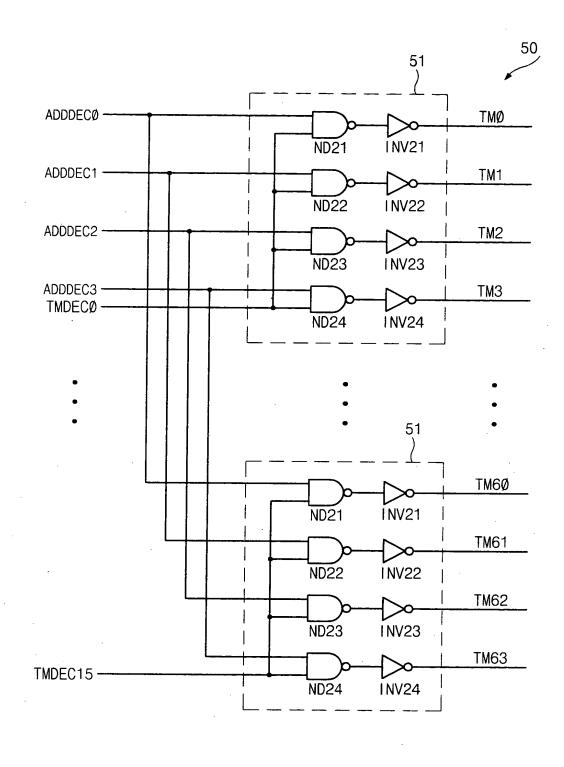


Fig.7

Attorney Docket No.: 40296-0032

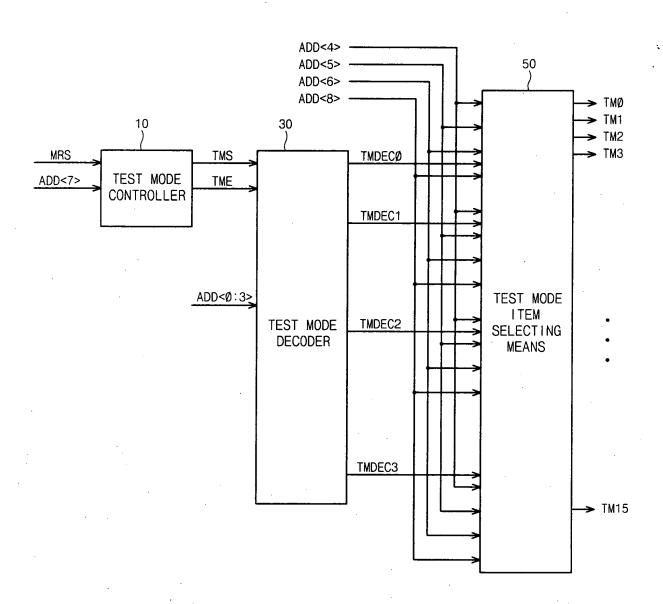


Fig.8